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Despite its continuing popularity, the so-called standard circuit model of compound semiconductor field-effect transistors (FETs) and high electron mobility transistors (HEMTs) is shown to have a limitation for nonlinear analysis and design: it is valid only in the static limit. When the voltages and currents are time-varying, as they must be for these devices to have any practical use, the model progressively fails for higher specification circuits. This book shows how to reform the standard model to render it fully compliant with the way FETs and HEMTs actually function, thus rendering it valid dynamically. Proof-of-principle is demonstrated for several practical circuits, including a frequency doubler and amplifiers with demanding performance criteria. Methods for extracting both the reformulated model and the standard model are described, including a scheme for re-constructing from S-parameters the bias-dependent dynamic (or RF) I(V) characteristics along which devices work in real-world applications, and as needed for the design of nonlinear circuits using harmonic-balance and time-domain simulators. The book includes a historical review of how variations on the standard model theme evolved, leading up to one of the most widely used: the Angelov (or Chalmers) model.

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An expert guide to understanding and making optimum use of BSIM Used by more chip designers worldwide than any other comparable model, the Berkeley Short-Channel IGFET Model (BSIM) has, over the past few years, established itself as the de facto standard MOSFET SPICE model for circuit simulation and CMOS technology development. Yet, until now, there have been no independent expert guides or tutorials to supplement the various BSIM manuals currently available. Written by a noted expert in the field, this book fills that gap in the literature by providing a comprehensive guide to understanding and making optimal use of BSIM3 and BSIM4. Drawing upon his extensive experience designing with BSIM, William Liu provides a brief history of the model, discusses the various advantages of BSIM over other models, and explores the reasons why BSIM3 has been adopted by the majority of circuit manufacturers. He then provides engineers with the detailed practical information and guidance they need to master all of BSIM's features. He: Summarizes key BSIM3 components Represents the BSIM3 model with equivalent circuits for various operating conditions Provides a comprehensive glossary of modeling terminology Lists alphabetically BSIM3 parameters along with their meanings and relevant equations Explores BSIM3's flaws and provides improvement suggestions Describes all of BSIM4's improvements and new features Provides useful SPICE files, which are available online at the Wiley ftp site